



## **ARTEMIS, ENIAC and the Austrian Funding Context**

**Veranstaltung Europäische  
Technologieplattformen  
CATT/WKOÖ**

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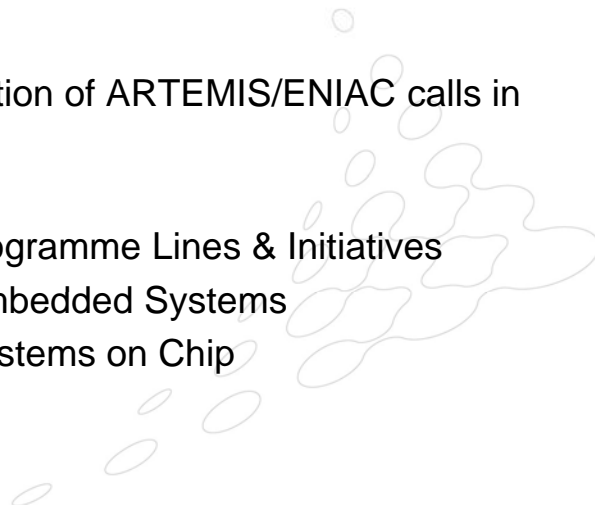
Georg Niklfeld



### **Overview**



- Where we stand
- Implementation of ARTEMIS/ENIAC calls in Austria
- National Programme Lines & Initiatives
  - FIT-IT Embedded Systems
  - FIT-IT Systems on Chip



**2 completed calls in ARTEMIS & ENIAC,  
3rd calls currently open**



- ARTEMIS call 2010:  
Project outline submission 26 March, FPPs 1 September
- ENIAC call 2010:  
Project outline submission 30 April, FPPs 30 July
- Already funded ARTEMIS projects with Austrian participation:
  - CESAR: Cost-efficient methods and processes for safety relevant embedded systems
  - INDEXYS: Industrial exploitation of the GENESYS cross-domain architecture
  - ACROSS: ARTEMIS Cross-Domain Architecture
  - POLLUX: Process Oriented Electrical Control Units for Electrical Vehicles developed on a multi-system real-time embedded platform
  - R3COP: Robust & Safe Mobile Co-operative Autonomous Systems

**Austrian Implementation of ARTEMIS/ENIAC calls**



- bmvit is the national programme owner
- FFG is the National Funding Authority (NFA) for ARTEMIS/ENIAC in Austria
- Proposal submission and evaluation are centralised at the Joint Undertaking / in Brussels
- **But** (only) the NFA performs cost checks and cost monitoring, according to national rules
- NFA disburses national funds to local participants
- In sum, the FFG is heavily involved when you implement ARTEMIS / ENIAC projects in Austria!

## Services by the FFG



- [www.ffg.at/artemis](http://www.ffg.at/artemis) [www.ffg.at/eniac](http://www.ffg.at/eniac)  
Official call information for Austrian participants
- Consultation and advice for Austrian proposers
- Austrian proposers must submit a cost sheet (,Part C') conformant to Austrian funding rules with the Full Project Proposal
- FFG checks the eligibility of Austrian costs and subsequently supports bmvit in the evaluation process
- FFG provides a bilateral funding contract to each national participant, covering the national component of their funding
- Based on this contract, the FFG performs cost verification during proposal implementation and disburses national funds

## ARTEMIS/ENIAC: Austrian Funding Rates



	Large Enterprises, Groups and Associations of Enterprises	Medium Enterprises	Small Enterprises	Public Research Institutes and Universities
Percentage of the national subsidy to the beneficiaries	25%	35%	45%	59%
+ JU Funding	16,7%			

## National Level: the FIT-IT programme



### FIT-IT

Create radically new IT  
(research prototype)

Strengthen the  
competitiveness of  
Austrian research and industry

Train qualified researchers

Improve international visibility

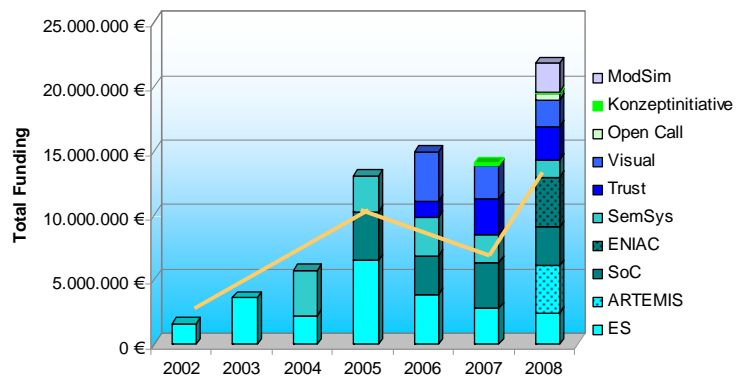


## Zugeweilte Fördermittel in FIT-IT



### FIT-IT

Fördersummen in FIT-IT



### Reasons for the success of FIT-IT ES



- Diverse landscape of innovative firms
  - Semiconductor manufacturers
  - Specialised SMEs
  - System providers
  - Production technology manufacturers
- Internationally established research institutions
  - Good results in EU-FP-calls

### FIT-IT ES Research topics



#### Research Topics

- Hard real-time: WCET, modularity, SW design, modularity, composability
- HW aspects, HW/SW co-design
- Testing
- Security
- Limited resources, power management
- Sensor systems / integration
- Communication, protocols

## **FIT-IT Embedded Systems – Topics of Funded Projects**



- 14 Component technologies for (wireless) communications
- 9 Component technologies for the transport industry
- 8 Network architectures and network components
- 5 Smart environments
- 4 Manufacturing technologies
- 3 Smart card technologies

## **Synergies national/transnational programmes**



- Since 2009, joint calls for „Embedded Systems & Semiconductors“ in FIT-IT
- Timing co-ordinated with ARTEMIS/ENIAC schedules
- National funding lines as a breeding ground for successful participation in large transnational activities

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Seite 13